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Electronic Version v1.1

Stylesheet Version v1.1.0

Title o	f
Invention	n

STRUCTURE AND METHOD OF MAKING STRAINED CHANNEL CMOS TRANSISTORS HAVING LATTICE-MISMATCHED EPITAXIAL EXTENSION AND SOURCE AND DRAIN REGIONS

Application Number:

Date:

First Named Applicant:

Huajie Chen

Confirmation Number:

Attorney Docket Number: F

FIS920030198US1

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Submitted By:	Elec. Sign.	Sign. Capacity
H. Daniel Schnurmann	H. Daniel	Agent
Registered Number: 35791	Schnurmann /s/	

Documents being submitted:	Files
us-fee-sheet	FIS920030198US1-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
us-assignment	FIS920030198US1-usassn.xml
	us-assignment.xsl
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	FIS920030198US1Assignment1.tif
	FIS920030198US1Assignment2.tif
us-request	FIS920030198US1-usrequ.xml
	us-request.dtd
	us-request.xsl
us-declaration	FIS920030198US1Declaration2.tif
us-declaration	FIS920030198US1Declaration3.tif
application-body	FIS920030198-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
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	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
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	mmlextra.ent
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	soextblx.dtd
	FIS920030198US1Drawing1.tif
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us-declaration	FIS920030198US1Declaration1.tif

Comments
Assignee: International Business Machines Corporation, Armonk, New York